



Unchained Skies: A Deep Dive into Reverse Engineering and Exploitation of Drones

Nico Schiller

Moritz Schloegel

Who we are

Nico Schiller

- researcher @ CISPA
- interested in drones and their security
- fuzz all the things!

Moritz Schloegel

- also researcher @ CISPA
- interested in automated bug finding, mostly fuzzing
- obfuscation / deobfuscation (Next-gen VMs talk at REcon22)



Consumer Drones



Why Drones?





Where things can go wrong: Airports

The Washington Post

TRANSPORTATION

Drone sighting briefly stops air traffic at Reagan National

Some flights were delayed after arrivals and departures were temporarily halted

By Katherine Shaver

Where things can go wrong: Airports

The Washington Post

Drone sighting briefly stops air traffic at Reagan Nat



Bill Bostock Feb 21, 2019, 2:02 PM GMT+1



Dublin Airport briefly shut down over a drone sighting at the runway

Planes from the flag carrier airline of Ireland Aer Lingus at Dublin Airport. Getty

Where things can go wrong: Airports

TRANSPORTATION

The Washington Post Drone sighting briefly stops air traffic at Reagan Nat ME > TRANSPORTATIO Some flights were delayed after a Dublin Airport briefly shut down over a drone sighting at the runway By Katherine Shaver Bill Bostock Feb 21, 2019, 2:02 PM GMT+1 July 21, 2022 at 2:30 p.m. EDT Gatwick drone disruption cost airport just £1.4m Airlines bear brunt of cost with easyJet alone putting its Planes from the flag carrie compensation bill and lost revenue at £15m Arrivals

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Cancelled

Page 3 of 3

Where things can go wrong: Prisons

JEFF LINK BUSINESS 29.87.2822 12:88 PM

Drone Contraband Deliveries Are Rampant at US Prisons

Law enforcement officers face an air assault as drugs, weapons, and phones are flown in to prisoners.



Where things can go wrong: Prisons

JEFF LINK BUSINESS 29.07.2022 12:00 PM

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Law enforcement officers face an air assault as drugs, weapons, and phones are flown in to prisoners.



Increase in use of drones for prison smuggling

() 4 April



The number of drones caught flying into Scottish prisons is increasing, new figures show.



Low entry barrier for air mobility in a traditionally heavily regulated sector!

Recent Scenario: Conflicts

Ukraine sends 300 DJI Mavic 3T drones to battle Russians ahead of expected offensive

Bruce Crumley | Mar 31 2023 - 3:39 am PT 🛛 厚 3 Comments



In another setback to global **drone giant DJI's** efforts to keep its consumer and enterprise products from being used in the conflict provoked by Russia's invasion of **Ukraine**, officials in Kyiv said this week a small army of 300 Mavic 3T UAVs had been procured and sent to the eastern front in the space of just a few days.

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WSJ NEWS EXCLUSIVE | WORLD

Chinese Drones Still Support Russia's War in Ukraine, Trade Data Show

Despite sanctions, Kremlin continues to deploy small unmanned Chinese aircraft

By <u>Benoit Faucon</u> Follow in Dubai and <u>Ian Talley</u> Follow in Washington Updated Feb. 18, 2023 10:01 am ET

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Ukraine rapidly expanding its 'Army of Drones' for front line

3 26 April

WSJ NEWS EXCLUSIVE | WORLD

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The Drone War in Ukraine Is Cheap, Dead

Cro

By Faine Greenwood, an ex



opular hobby drones in the world used for filming

on the front line is the DJI Mavic which costs

Last year, its Chinese manufacturer banned exports to Ukraine and Russia insisting its products are "for civilian use only".

Slava says the ban has made it harder to get hold of the drones but Ukraine has still been able to import thousands.











Software limits Geofencing







Software limits Geofencing



Hardware protection No debug interfaces



Tracking and Identification

- Drones broadcast information
 - Serial number
 - Position
- Tracking via DJI Aeroscope (recently deprecated)
- New regulations mandate tracking

=> Quick identification and localization!



Software Protection

- Height and range limitations
 - height: maximum 500m
 - but: safety warning above 120m
 - range: currently unlimited
- Speed limits
- No-Fly Zones

Safety	Control	Camera	Transmission	About		
Flight Protection						
Max Altitude		120 m ——	•			
Max Distance		2000 m	•			
Auto RTH Altitude		15 m 🔵				
When returning to home, the aircraft will rise to a preset altitude before returning.						
Update Home Poi	nt			>		

S	afety	Control	Camera	Transmission	About
Max	You are n				
Max	m (400 ft regulation doing so, the result) may violate loca s or subject to re you bear full resp ing impact. Ely wi	-•		
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When .	Cano	el nome, are arerar	Agree	eset altitude before re	eturning.
Updat	e Home P	oint			



https://fly-safe.dji.com/nfz/nfz-query

Hardware Protection

- disabled debug interfaces
- firmware
 - \circ closed source
 - encrypted
 - signed
- proprietary communication protocol

INFO:Platform early init begin INFO:Target early init begin DEBUG:boot args 0x40110000 0x0 0x55f20 0x55f20 INIT: cpu 0, calling hook 0x433fd (version) at level 0x3ffff, flags 0x1 INFO:version:

arch: ARM platform: SPARROW target: SPARROW UAV project: SPARROW UAV TEST buildid: J9H88 LOCAL buildtime:Sep 17 2020 16:17:53 DEBUG:initializing heap DEBUG:calling constructors DEBUG:initializing mp DEBUG:initializing threads DEBUG:initializing timers DEBUG:initializing ports DEBUG:creating bootstrap completion thread DEBUG:top of bootstrap2() CONTROL 0x0 INFO: initializing platform

INFO:lcs should be production INFO:jtag will be disabled

INFO:initializing target spi_master_get: spi master id :0 INFO:spiflash id : ef4018 DEBUG:cmp status(0) is ok DEBUG:spiflash_cmp_status_select=>ok DEBUG:BP status is ok(status:34, val:d) DEBUG:spiflash_bp_status_select=>ok DEBUG:spiflash_wp_portion_select=>ok DEBUG:spiflash_write_protection_init=>ok INFO:target init INFO:lcs should be production





Software limits Geofencing



Hardware protection No debug interfaces



Let's see if these countermeasures are good enough



• Market share (94% Consumer)



- Market share (94% Consumer)
- Security-conscious
 - Whitepaper
 - Bug bounty program



- Market share (94% Consumer)
- Security-conscious
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Analyzed Drones: Mini 2, Mavic Air 2, Mavic 2



Drone Hardware Overview





- Eavesdropping
- Signal analysis
- Tracking
- Protocol knowledge



- Eavesdropping
- Signal analysis

• Tracking

• Protocol knowledge

Hardware



- PCB analysis
- Component lookup
- Debug interfaces
- Firmware dumping
- Memory dumping



- Eavesdropping
- Signal analysis

• Tracking

Protocol knowledge

Hardware



- PCB analysis
- Component lookup
- Debug interfaces
- Firmware dumping
- Memory dumping

Firmware



- Reverse engineering
- Fuzzing
- Privilege escalation
- Firmware reflashing
- Disable software limits



- Eavesdropping
- Signal analysis

• Tracking

Protocol knowledge

Hardware



- PCB analysis
- Component lookup
- Debug interfaces
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- Memory dumping

Firmware



- Reverse engineering
- Fuzzing

• Privilege escalation

- Firmware reflashing
- Disable software limits

Wireless Physical Layer Reversing DJI DroneID

Static Analysis Hands on the Drone

Dynamic Analysis Fuzzing Drones for Pain and Profit



How to listen on the Wireless Physical Layer ...

How to listen on the Wireless Physical Layer ...



Software Defined Radio (SDR)

How to listen on the Wireless Physical Layer ...





Software Defined Radio (SDR)

Signal Analyzer Software (e.g., baudline, inspectrum)


























Capture Raw Signal Data





Capture Raw Signal Data





Capture Raw Signal Data



Fixed Synchronization Symbols "Zadoff-Chu Sequences"



Capture Raw Signal Data







Shift mask, correlate both parts

Time synchronisation via cyclic prefixes

Capture Raw Signal Data Capture Raw Capture Raw Detection

Synchronization







Received DroneID packet: "pkt len": 88, "unk": 16, "version": 2, "sequence_number": 749, "state_info": 8183, "serial_number": "1k N1", "longitude": 7.267175834942389, "latitude": 51.44635111984553, "altitude": 40.84, "height": 3.66, "v_north": -1, "v_east": 0, "v_up": -1, "d_1_angle": -14958, "gps_time": 1649869492647, "app_lat": 51.446316742392554, "app_lon": 7.267101350460944, "longitude_home": 7.267170105366893, "latitude_home": 51.44636830857202, "device_type": "Mavic Air 2", "uuid len": 19, "uuid": " ۳. "crc-packet": "267c", "crc-calculated": "267c"























Received DroneID packet:

"longitude": 7.267175834942389, "latitude": 51.44635111984553,





Received DroneID packet:

"longitude": 7.267175834942389, "latitude": 51.44635111984553, "app_lat": 51.446316742392554, "app_lon": 7.267101350460944, "longitude_home": 7.267170105366893, "latitude_home": 51.44636830857202,





Received DroneID pa



Summary: Wireless Physical Layer

- Much information is broadcast, including:
 - Drone location
 - Pilot location
 - Serial number
- Signal not encrypted
- But: Easy to spoof the pilot location

Wireless Physical Layer Reversing DJI DroneID

Static Analysis Hands on the Drone

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Analyze PCB







Analyze PCB



Found Boot Screen (UART)!

			> Trigger View 🛦	8
Async Serial			Data 🕐 🥑	⊞ ⊵
 Input Channel * Bit Rate (Bits/s)	03. Channel 3 926300		DEBUG:spiflash_bp_status_select=>ok DEBUG:spiflash_wp_portion_select=>ok DEBUG:spiflash_write_protection_init=>ok INF0:target init (\0\0@\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0	(0\00\ (0\0\0 (0400
Bits per Frame	8 Bits per Transfer (Standard)		3 0./!249suwy=!PRs{`}Z INFO:Platform early init begin	
Stop Bits	1 Stop Bit (Standard)	99+ "JZ\r\nINFO:Platforn	INF0:Target early init begin DEBUG:boot args 0x40110000 0x0 0x56b28 0x56b2 INIT: cpu 0, calling hook 0x43d21 (version) a el 0x3ffff, flags 0x1	18 at lev
Parity Bit	No Parity Bit (Standard)		arch: ARM platform: SPARROW	
Significant Bit	Least Significant Bit Sent First (Standard)		target: SPARROW_RC project: SPARROW_RC_TEST buildid: K326J_LOCAL	
Signal inversion	Non Inverted (Standard)		Dulidime:Mar 2 2021 14:38:46 DEBUG:initializing heap DEBUG:calling constructors	
Mode	Normal		DEBUG:initializing mp DEBUG:initializing threads DEBUG:initializing timers	
	 Show in protocol results table Stream to terminal 		DEBUG:Initializing ports DEBUG:creating bootstrap completion thread DEBUG:creating bootstrap2() CONTROL 0x0 INF0:initializing platform INF0:ltag will be disabled INF0:initializing target spi master get: spi master id :0 INF0:spiflash id : ef4018 DEBUG:spiflash is ok(status:34, val:d) DEBUG:spiflash bp_status_select=>0k DEBUG:spiflash bp_status_select=>0k DEBUG:spiflash bp_status_select=>0k DEBUG:spiflash bp_status_select=>0k DEBUG:spiflash bp_status_select=>0k DEBUG:spiflash bp_status_select=>0k DEBUG:spiflash bp_status_select=>0k DEBUG:spiflash bp_status_select=>0k DEBUG:spiflash bp_status_select=>0k DEBUG:spiflash bp_status_select=>0k INF0:target init (000.3cs should be production 0./!249suwy=IPRs{}Z INF0:Target early init begin INF0:Target early init begin INF0:target init begin INF0:target oarly init begin INF0:target oarly init begin INF0:target early init begin INF0:target early init begin INF0:target early init begin INF0:tersion:	etion thread :0 sct=>ok 34, val:d) tc=>ok sct=>ok n

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Async Senai		INFO:version:			
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bit Rate (bits/s)	920300	project: SPARROW_UAV_TEST		\08\0\0\x02\0\0\0\0\x80\0\0\0\ 3	,0\0\x04\xFC\x0400.
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D		DEBUG:calling constructors			
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Significant Bit	Least Significant Bit Sent First	© DEBUG:initializing threads		project: SPARROW_RC_TES	
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		INFO:lcs should be production			

		INFO:Platform early init begin		
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Parity Bit	No Parity Bit (Standard)	DEBUG:calling constructors		
		DEBUG:initializing mp	platform: SPARROW target: SPARROW RC	
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Mode	Normal	DEBUG:creating bootstrap completion thread	DEBUG:initializing threads	
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Bit Rate (Bit/a) 92200 target: SPARROW_UAV 0/00000000000000000000000000000000000	Input Cl	Channel * 03. Channel 3	platform: SPARROW	 INFO:target init	
Bits per Transfer (Standard) builditine:Sep 17 2020 16:17:53 builditime:Sep 17 2020 16:17:53 Stop Bits 1 Stop Bit (Standard) DEBUG:initializing heap DEBUG:initializing threads Significant Bit Least Significant Bit Seet First (Standard) DEBUG:initializing threads DEBUG:initializing threads Significant Bit Least Significant Bit Seet First (Standard) DEBUG:initializing threads DEBUG:initializing threads Mode Normal DEBUG:initializing threads DEBUG:initializing threads DEBUG:initializing threads DEBUG:initializing ports DEBUG:initializing threads DEBUG:initializing threads DEBUG:initializing threads Mode Normal DEBUG:initializing ports DEBUG:initializing threads DEBUG:initializing threads VC: Stop Bit State Signal Inversion Non Inverted (Standard) DEBUG:creating bootstrap Completion thread DEBUG:initializing threads VC: Stop Bit State Signal Inversion Non Inverted (Standard) DEBUG:initializing threads DEBUG:initializing threads VC: Stop Bit State Signal Inversion Normal DEBUG:initializing threads DEBUG:initializing threads VC: Stop Bit State Other States DEBUG:initializing threads DEBUG:initializing threads DEBUG:initializing threads VC: Stop Bit States Signal Inversion 4 Signal Inversi	Bit Rate	e (Bits/s) 926300	target: SPARROW_UAV project: SPARROW_UAV_TEST	0(0)(0)(0)(0)(0)(0)(0)(0)(0)(0)(0)(0)(0)	0\0\0\0\0 FC\x0400.
Stop Bits 1 Stop Bit (Standard) DEBUG: initializing heap INT: cpu 0, f, calling hook 6x43d21 (version) at lev Party Bit No Party Bit (Standard) DEBUG: calling constructors arch: APM Significant Bit Least Significant Bit Standard) DEBUG: initializing threads arch: APM Signal Inversion Non Inverted (Standard) DEBUG: initializing threads DEBUG: initializing threads Mode Normal DEBUG: creating bootstrap completion thread DEBUG: initializing threads Mode Normal DEBUG: creating bootstrap (Normal) DEBUG: initializing threads INFO: lcs should be production INFO: lcs should be production INFO: lcs should be production INFO: lcs should be production INFO: lcs should be production INFO: lcs should be production INFO: lcs should be production INFO: lcs should be production INFO: lcs should be production INFO: lcs should be production INFO: initializing target Spin master (d: 0) is ok DEBUG: spiflash id : ef4018 DEBUG: spiflash id : ef4018 DEBUG: spiflash, crep status (o) is ok DEBUG: spiflash _crep status_select=>ok DEBUG: spiflash _crep status _select=>ok DEBUG: spiflash _crep status _select=>ok	Bits per	r Frame 8 Bits per Transfer (Standard)	buildid: J9H88_LOCAL buildtime:Sep 17 2020 16:17:53	0./12495uwy=!PKS{ }2 INF0:Platform early init begin INF0:Target early init begin DEBUG:boot args 0x40110000 0x0 0x56b28 0x	
Parity Bit No Parity Bit (Standard) DEBUG:calling constructors IHF9:version: APP Significant Bit Least Significant Bit Sent First (SDEBUG: initializing threads DEBUG: initializing threads DEBUG: initializing threads Signal inversion Non Inverted (Standard) DEBUG: initializing ports DEBUG: initializing ports DEBUG: initializing threads Mode Normal DEBUG: creating bootstrap completion thread DEBUG: initializing mp DEBUG: initializing threads Signal inversion Normal DEBUG: creating bootstrap COMPletion thread DEBUG: initializing mp Mode Normal DEBUG: creating bootstrap COMPletion thread DEBUG: initializing mp Signal inversion Show in protocol results table CONTROL 0x00 DEBUG: initializing platform INFO:ics should be production INFO:ics should be production INFO:initializing target Significant get: spi master id :0 INFO:initializing target Spi_master_get: spi master id :0 INFO:initializing of the folde DEBUG: creating initializing target Spi_master_get: spi master id :0 INFO:pitash with oprotoci select=>ok DEBUG: spiflash _cropet status (0) is ok DEBUG: spiflash _cropet status select=>ok	Stop Bit	its 1 Stop Bit (Standard)	DEBUG:initializing heap	<pre>INIT: cpu 0, calling hook 0x43d21 (versio el 0x3ffff, flags 0x1</pre>	
Significant Bit Least Significant Bit Sent First (S260 LOCAL Signal Inversion Non Inverted (Standard) DEBUG: initializing timers Mode Normal DEBUG: creating bootstrap completion thread DEBUG: initializing timers DEBUG: initializing timers DEBUG: creating bootstrap completion thread DEBUG: initializing timers DEBUG: initializing ports DEBUG: creating bootstrap completion thread DEBUG: initializing timers DEBUG: initializing ports DEBUG: creating bootstrap completion thread DEBUG: initializing timers DEBUG: initializing ports CONTROL 0x0 DEBUG: initializing ports DEBUG: initializing timers INFO: initializing platform INFO: initializing grade INFO: initializing grade DEBUG: initializing timers INFO: initializing target Stream to terminal INFO: initializing target DEBUG: initializing target INFO: initializing target spi master id :0 INFO: initializing target DEBUG: inplash id : ef4018 DEBUG: inplash id : ef4018 DEBUG: inplash id : ef4018 DEBUG: cmp status (0) is ok DEBUG: inplash id : ef4018 DEBUG: inplash id : ef4018 DEBUG: inplash id : ef4018 DEBUG: isplfash (d) : ef4018 DEBUG: inplash (d) : ef4018 DEBUG: inplash (d) : ef4018	Parity B	Bit No Parity Bit (Standard)	DEBUG:calling constructors DEBUG:initializing mp	INFO:version: arch: ARM platform: SPARROW tarnet: SPARROW PC	
Signal inversion Non Inverted (Standard) DEBUG:initializing ports DEBUG:initializing theap Mode Normal DEBUG:creating bootstrap completion thread DEBUG:initializing threads DEBUG:initializing threads DEBUG:initializing threads DEBUG:initializing threads OCONTROL 0x0 ONTROL 0x0 DEBUG:creating bootstrap() INFO:initializing platform INFO:initializing platform INFO:initializing threads INFO:initializing platform INFO:initializing target INFO:initializing target signalizing target INFO:initializing target INFO:initializing target signalizing target INFO:spiflash id : ef4018 DEBUG:spiflash ig status select=>ok DEBUG:spiflash cmp_status_select=>ok DEBUG:spiflash the production init=>ok	Signific	cant Bit Least Significant Bit Sent First (S	DEBUG:initializing threads DEBUG:initializing timers	 project: SPARROW RC TEST buildid: K326J LOCAL buildtime:Mar Z 2021 14:38:46	
Mode Normal DEBUG:creating bootstrap completion thread DEBUG:top of bootstrap2() DEBUG:initializing threads DEBUG:initializing ports Show in protocol results table CONTROL 0x0 DEBUG:creating bootstrap2() DEBUG:creating bootstrap2() INFO:initializing platform INFO:initializing platform DEBUG:creating bootstrap2() DEBUG:creating bootstrap2() INFO:initializing platform INFO:initializing platform INFO:initializing platform DEBUG:creating bootstrap2() INFO:initializing platform INFO:initializing platform INFO:initializing platform INFO:initializing platform INFO:initializing target INFO:initializing target INFO:initializing target INFO:initializing target spi_master_get: spi master id :0 INFO:spiflash id : ef4018 DEBUG:crep status(0) is ok DEBUG:spiflash id: select=>ok DEBUG:cspiflash_cmp_status_select=>ok DEBUG:spiflash_cmp_status_select=>ok DEBUG:spiflash wp portion select=>ok	Signal i	inversion Non Inverted (Standard)	DEBUG:initializing ports	DEBUG:initializing heap DEBUG:calling constructors	
Image: Node DEBUG:top of bootstrap2() DEBUG:initializing timers Show in protocol results table CONTROL 0x0 DEBUG:creating bootstrap2() Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal DEBUG:top of bootstrap2() Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal DEBUG:top of bootstrap2() Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal Image: Stream to terminal I	Mode	Normal	DEBUG:creating bootstrap completion thread	DEBUG:initializing mp DEBUG:initializing threads	
Show in protocol results table Stream to terminal INFO:initializing platform INFO:initializing target Stream to terminal INFO:initializing target Spi_master_get: spi master id :0 INFO:spiflash id : ef4018 DEBUG:cmp status(0) is ok DEBUG:spiflash_cmp_status_select=>ok DEBUG:spiflash_vp.portion_select=>ok DEBUG:spiflash_vp.portion_select=>ok			DEBUG:top of bootstrap2()	DEBUG:initializing timers DEBUG:initializing ports	
Stream to terminal INFO:Initializing platform CONTROL 6x0 INFO:Ics should be production INFO:Ics should be production INFO:Ics should be production INFO:initializing target INFO:initializing target INFO:spiflash id : ef4018 Spi_master_get: spi master id :0 INFO:spiflash id : ef4018 DEBUG:cmp status(0) is ok DEBUG:cmp status(0) is ok DEBUG:cmp status_select=>ok DEBUG:spiflash wp.portion select=>ok		Show in protocol results table	CONTROL 0x0	DEBUG:creating bootstrap completion threa DEBUG:top of bootstrap2()	
DEBUG:BP status is ok(status:34, val:d) 0.//249sumy=1PRs{ }Z DEBUG:spiflash_bp_status_select=>ok INF0:Platform early init begin DEBUG:spiflash_wp_portion_select=>ok DEBUG:spiflash_wp_ortion_select=>ok DEBUG:spiflash_wp_portion_select=>ok INT: cpu 0, calling hook 0x43d21 (version) at lev		✓ Stream to terminal	INFO:initializing platform INFO:lcs should be production INFO:jtag will be disabled INFO:initializing target spi_master_get: spi master id :0 INFO:spiflash id : ef4018 DEBUG:cmp status(0) is ok DEBUG:spiflash_cmp_status_select=>ok DEBUG:BP status is ok(status:34, val:d) DEBUG:spiflash_bp_status_select=>ok DEBUG:spiflash_wp_portion_select=>ok	CONTROL 0x0 INFO:Los should be production INFO:Los should be production INFO:Iscs should be production INFO:its should be production INFO:intializing target spi master get: spi master id :0 INFO:spiflash id : ef4018 DEBUG:spiflash cmp_status_select=>ok DEBUG:spiflash portion_select=>ok DEBUG:spiflash write_protection_init=>ok DEBUG:spiflash write_protection_init=>ok DEBUG:spiflash write_protection_ 0./1249suwy=1PRs{}2 INFO:Target anit VMO:Target early init begin INFO:Target early init begin INF	
DEBUG:spiriasn_write_protection_init=>ok INF0:version: arch: ARM			INFO:target init	INFO:version: arch: ARM	

			INFO:Platform early init begin			
			INFO:Target early init begin		> Trigger View A	0
			DEBUG:boot args 0x40110000 0x0 0x55f20 0x55f20			
	Async Serial		INIT: cpu 0, calling hook 0x433fd (version) at level 0x3ffff, flags 0x1		Data 🕐 🥝	III 🖂
	Adjilo ochu		INFO:version: arch: ARM		DEBUG:spiflash_bp_status_select=>0 DEBUG:spiflash_wp_portion_select=>	k ok
`	Input Channel *	03. Channel 3	platform: SPARROW	_	INFO: target init	2) 0) 0) xC0 () 0) 00)
	Bit Rate (Bits/s)	926300	target: SPARROW_UAV project: SPARROW_UAV_TEST		0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\0\	0\0p\0\0\0\0\0\0 0\x04\xFC\x0400.
	Bits per Frame	8 Bits per Transfer (Standard)	buildid: J9H88_LOCAL buildtime:Sep 17 2020 16:17:53		0./1249suwy=1PRs{ }2 INF0:Platform early init begin INF0:Target early init begin DEBUG:boot args 0x40110000 0x0 0x5	
	Stop Bits	1 Stop Bit (Standard)	DEBUG:initializing heap			
	Parity Bit	No Parity Bit (Standard)	DEBUG:calling constructors DEBUG:initializing mp		INFO:version: arch: ARM platform: SPARROW target: SPARROW BC	
	Significant Bit	Least Significant Bit Sent First (DEBUG:initializing threads DEBUG:initializing timers		project: SPARROW RC TEST buildid: K326J LOCAL buildtime:Mar Z 2021 14:30	
	Signal inversion	Non Inverted (Standard)	DEBUG:initializing ports		DEBUG:initializing heap DEBUG:calling constructors	
	Mode	Normal	DEBUG:creating bootstrap completion thread		DEBUG:initializing threads	
		Ob			DEBUG:initializing ports DEBUG:creating bootstrap completio	
		Show in protocol results table	INFO: initializing platform		DEBUG:top of bootstrap2() CONTROL 0x0	
		Stream to terminal	INFO:initializing platform INFO:lcs should be production INFO:jtag will be disabled INFO:initializing target spi_master_get: spi master id :0 INFO:spiflash id : ef4018 DEBUG:cmp status(0) is ok DEBUG:spiflash_cmp_status_select=>ok DEBUG:BP status is ok(status:34, val:d) DEBUG:spiflash_bp_status_select=>ok DEBUG:spiflash_wp_portion_select=>ok DEBUG:spiflash_write_protection_init=>ok INFO:target init INFO:lcs should be production		CONTROL 6x0 INFO: initializing platform INFO: ics should be production INFO: jtag will be disabled INFO: initializing target spi master_get; spi master id :0 INFO: spiflash id : ef4018 DEBUG: spiflash_cmp_status_select=>> DEBUG: spiflash_protion_select=>> DEBUG: spiflash_write_protection_in INFO: target init V000.3cs should be production 0./1249Suwy=IPRs[']2 INFO: Platform early init begin INFO: target early init begin INF	<pre>rap2() slatform production isabled target tamaster id :0 ef4018 is ok(status:34, val:d) status_select=>ok sortion_select=>ok production iz production iz phook 0x43d21 (version) at lev ex1 and and and and and and and and and and</pre>

		INFO:Platform early init begin			
		INFO:Target early init begin		> Trigger View A	0
		DEBUG:boot args 0x40110000 0x0 0x55f20 0x55f20			
Async Serial		INIT: cpu 0, calling hook 0x433fd (version) at level 0x3ffff, flags 0x1		Data 🕐 🥹	III 🖂
Input Channel *	03. Channel 3	INFO:version: arch: ARM		DEBUG:spiflash bp_status_select DEBUG:spiflash_wp_portion_selec DEBUG:spiflash_write_protection	=>ok t=>ok _init=>ok
Bit Rate (Bits/s)	926300	target: SPARROW_UAV project: SPARROW_UAV_TEST		\0\0@\0\0\0\0\0\0\0\0\0\0\0\0\0\0 0\0\0\0\	\x02\0\0\xC0 \0\00\ \0\0\0p\0\0\0\0\0 \0\0\x04\xFC\x0400.
Bits per Frame	8 Bits per Transfer (Standard)	buildid: J9H88_LOCAL buildtime:Sep 17 2020 16:17:53		0./!249suwy=!PRs{`}Z INFO:Platform early init begin INFO:Target early init begin DEFNIC:boot args 0x40110000 0x0	
Stop Bits	1 Stop Bit (Standard)	DEBUG:initializing heap		INIT: cpu 0, calling hook 0x43d el 0x3fff, flags 0x1	
Parity Bit	No Parity Bit (Standard)	DEBUG:calling constructors DEBUG:initializing mp		INFO:version: arch: ARM platform: SPARROW target: SPARROW RC	
Significant Bit	Least Significant Bit Sent First (\$	DEBUG:initializing threads DEBUG:initializing timers		project: SPARROW RC TE buildid: K326J LOCAL buildtime:Mar Z 2021 1	
Signal inversion	Non Inverted (Standard)	DEBUG:initializing ports		DEBUG:initializing heap DEBUG:calling constructors DEBUG:initializing mp	
Mode	Normal	DEBUG:top of bootstrap2()		DEBUG: initializing threads DEBUG: initializing timers	
	🛂 Show in protocol results table	CONTROL 0x0		DEBUG:creating bootstrap comple DEBUG:top of bootstrap?()	
	Stream to terminal	INFO:initializing platform INFO:lcs should be production INFO:jtag will be disabled INFO:initializing target spi_master_get: spi master id :0 INFO:spiflash id : ef4018 DEBUG:cmp status(0) is ok DEBUG:spiflash_cmp_status_select=>ok DEBUG:spiflash_cmp_status_select=>ok DEBUG:spiflash_bp_status_select=>ok DEBUG:spiflash_bp_status_select=>ok DEBUG:spiflash_wp_portion_select=>ok DEBUG:spiflash_write_protection_init=>ok INFO:target init	DEBUG:top of boo CONTROL 0x0 INF0:initalizini INF0:lcs should INF0:lcs should INF0:lcs should INF0:lcs yall b INF0:spiflash ud DEBUG:spiflash ud DEBUG:spiflash b DEBUG:spiflash b DEBUG:spiflash DEBUG:spiflash UNF0:target init 0./1249Suwy=IPRS: INF0:target earl DEBUG:sbot args INF0:target earl DEBUG:sbot args INF0:tersion: arch:	DEBUG: Top of Bootstrap2() CONTROL 8x8 INF0:initializing platform INF0:jtag will be disabled INF0:initializing target spi master_get: spi master id : INF0:spiflash id : ef4018 DEBUG: mp status[0] is ok DEBUG: mp status[0] is ok DEBUG: spiflash cmp status_select DEBUG: spiflash wp portion selec DEBUG: spiflash wp portion select DEBUG: spiflash wite protection INF0: target init v000.3cs should be production 0./1249sumy=IPAS{}2 INF0: Platform early init begin DEBUG: boot args 6x40110000 6x0 INTT: cpu 0, calling hook 0x43d el 0x3fff, flags 0x1 INF0: version: arch: ARM	0 t=>ok , val:d) =>ok t=>ok _init=>ok 0x56b28 0x56b28 21 (version) at lev
			INFO:Platform early init begin		
---	-------------------	------------------------------------	---	---	--------------------------------------
			INFO:Target early init begin	> Trigger View A	8
			DEBUG:boot args 0x40110000 0x0 0x55f20 0x55f20		
	Async Serial		INIT: cpu 0, calling hook 0x433fd (version) at level 0x3ffff, flags 0x1	Data 🕐 🥑	III 🖂
			INFO:version:		⊳ok
1			arch: ARM	DEBUG:spiflash wp portion select DEBUG:spiflash write protection	
	Input Channel *	03. Channel 3	platform: SPARROW	INF0:target init	(02\0\0\xC0 \0\00\
	Bit Rate (Bits/s)	926300	target: SPARROW_UAV	0\0\0\0\0\0\0\0\XFF\x08\0\x8C\0\0\	0\0\0p\0\0\0\0\0\0
			project: SPARROW_UAV_TEST		
	Bits per Frame	8 Bits per Transfer (Standard)	buildid: J9H88_LOCAL	INFO:Platform early init begin	
	04 04-	1 04 Dit (04 dd)	buildtime:Sep 17 2020 16:17:53	DEBUG:boot args 0x40110000 0x0 0x	
	Stop Bits	T Stop Bit (Standard)	DEBUG:initializing heap	el 0x3ffff, flags 0x1	
	Parity Bit	No Parity Bit (Standard)	DEBUG:calling constructors	arch: ARM	
			DEBUG:Initializing mp	target: SPARROW_RC	
	Significant Bit	Least Significant Bit Sent First (DEBUG:initializing timers	project: SPARROW_RC_TES buildid: K326J_LOCAL	
	Signal inversion	Non Inverted (Standard)	DEBUG:initializing ports	buildtime:Mar 2 2021 14: DEBUG:initializing heap	
	olgital inversion	(calledid)	DEBUG:creating ports	DEBUG:calling constructors DEBUG:initializing mp	
	Mode	Normal	DEBUG:top of hootstrap2()	DEBUG:initializing threads DEBUG:initializing timers	
		Show in protocol results table	CONTROL 0x0	DEBUG:initializing ports DEBUG:creating bootstrap completi	
			INFO: initializing platform	DEBUG:top of bootstrap2() CONTROL 0x0	
		Stream to terminal	INFO:lcs should be production	INFO:initializing platform INFO:lcs should be production	
			INFO:itag will be disabled	INFO: jtag will be disabled	
			INFO:initializing target	spi_master_get: spi_master_id :0 INFO:spiflash_id : ef4018	
			spi master get: spi master id :0	DEBUG:cmp status(0) is ok DEBUG:sniflash cmp status select-	
			INFO:spiflash id : ef4018	DEBUG:BP status is ok(status:34,	
			DEBUG:cmp status(0) is ok	DEBUG:spiflash wp portion select	
			DEBUG:spiflash_cmp_status_select=>ok	INFO:target init	
			DEBUG:BP status is ok(status:34, val:d)	0./1249suwy=1PRs{`}Z	
			DEBUG:spiflash_bp_status_select=>ok	INFO: Target early init begin	
			DEBUG:spiflash_wp_portion_select=>ok	INIT: cpu 0, calling hook 0x43d2	(Sob28 0x56b28 L (version) at lev
			DEBUG:spiflash_write_protection_init=>ok		
			INFO:target init	arch: ARM	
			INFO:lcs should be production		











Analyze PCB Found Boot Screen (UART)! Check Bootloader Firmware



Analyze PCB



Found

Boot Screen

(UART)!



Check Bootloader Firmware Some Magic Values to Unlock Bootloader?!

100

```
fseek(file_descriptor,0,2);
filesize = ftell(file_descriptor);
fseek(file_descriptor,0,0);
printf("The file size is:%ld\n",filesize);
fread(&file_data,filesize,1,file_descriptor);
MAGIC_DATA_J = 0x7c2a5242;
Mem_filesize = filesize;
checksum_filedata = checkSum(&file_data);
checksum_filedata = checkSum(&file_data);
checksum_MAGIC_DATA_J = checkSum(&MAGIC_DATA_J,0xc);
MAGIC_DATA_D._0_4_ = (__sighandler_t)0x7c2a5260;
usb_if_transfer = (int *)libusb_alloc_transfer(0);
```





Modify

Firmware

```
uint uVarl;
```

```
uVarl = read_volatile_4(global_mp_state_mem);
return uVarl & Oxff;
```



```
{
uint uVarl;
```

```
uVarl = read_volatile_4(global_mp_state_mem);
return uVarl & Oxff;
```







Unsigned (Patch) Files?!



Modify Firmware





Summary: Static Analysis

• Full control over the transceiver SoC -> next target: main SoC

- Static analysis was key for all other steps
 - For example, when reversing the signal:
 - We needed seeds hidden in the firmware
 - Confirm DroneID packet structure

Wireless Physical Layer Reversing DJI DroneID

Static Analysis Hands on the Drone

Dynamic Analysis Fuzzing Drones for Pain and Profit

















Problems:

- drone != a single binary
 - complex firmware (multiple SoC's, different OSes)
 - hard to emulate
- no source code we could instrument

=> no easy off-the-shelf fuzzing solution available

Idea: Let's target communication protocol



DJI DUML Protocol















Prerequisites:

• A drone and fuzzer



- A drone and fuzzer
- Protocol knowledge



- A drone and fuzzer
- Protocol knowledge
- Bug oracle



- A drone and fuzzer
- Protocol knowledge
- Bug oracle



- A drone and fuzzer
- Protocol knowledge
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- A drone and fuzzer
- Protocol knowledge
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Prerequisites:

- A drone and fuzzer
- Protocol knowledge
- Bug oracle



Reproducible bugs!
ID	Oracle	Component	Observable Behavior	Classification	Severity	Remote	Vulnerable Devices
#1	ADB check	dji_sys binary	ADB started (root access)	arbitrary code exec			Mini 2
#2	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#3	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#4	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#5	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#6	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#7	crash	flight controller	critical error (drone reboot)	denial of service			Mini 2
#8	crash	flight controller	critical error (drone reboot)	denial of service			Mini 2
#9	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#10	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#11	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#12	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#13	crash	flight controller	critical error (drone reboot)	denial of service			Mavic Air 2
#14	UI change	WiFi chip	change SSID	arbitrary code exec			Mini 2, Mavic 3
#15	UI change	flight controller	change serial number	identity spoofing			Mini 2

ID	Oracle	Component	Observable Behavior	Classification	Severity	Remote	Vulnerable Devices
#1	ADB check	dji_sys binary	ADB started (root access)	arbitrary code exec			Mini 2
#2	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#3	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#4	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#5	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#6	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
# 7	crash	flight controller	critical error (drone reboot)	denial of service			Mini 2
#8	crash	flight controller	critical error (drone reboot)	denial of service			Mini 2
#9	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#10	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#11	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#12	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#13	crash	flight controller	critical error (drone reboot)	denial of service			Mavic Air 2
#14	UI change	WiFi chip	change SSID	arbitrary code exec			Mini 2, Mavic 3
#15	UI change	flight controller	change serial number	identity spoofing			Mini 2

Arbitrary Code Execution

- found by UI oracle: fuzzer changed an immutable value
- missing sanitization of user-controllable input

=> Linux command injection

Arbitrary Code Execution

Goal: root privileges -> start adb server

Problem: command length limited to max 32 characters

=> transfer exploit script chunkwise

ID	Oracle	Component	Observable Behavior	Classification	Severity	Remote	Vulnerable Devices
#1	ADB check	dji_sys binary	ADB started (root access)	arbitrary code exec	mid	x	Mini 2
#2	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#3	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#4	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#5	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#6	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#7	crash	flight controller	critical error (drone reboot)	denial of service			Mini 2
#8	crash	flight controller	critical error (drone reboot)	denial of service			Mini 2
#9	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#10	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#11	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#12	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#13	crash	flight controller	critical error (drone reboot)	denial of service			Mavic Air 2
#14	UI change	WiFi chip	change SSID	arbitrary code exec			Mini 2, Mavic 3
#15	UI change	flight controller	change serial number	identity spoofing			Mini 2

Change Immutable Serial Number

Safety	Control	Camera	Transmission	About
Whh Aersion				-1.3.10
Battery SN			1077-64	GA40E52
Aircraft SN			зонснена	AIADOE
Flight Controller	SN		Secure	Storage?
Remote Controll	er SN		300CH800	018499.00
Camera SN			19FOHAKI	480 0 90

Change Immutable Serial Number

Safety	Control	Camera	Transmission	About
App version				-1.3.10
Battery SN			30FP+8H	5A40E52
Aircraft SN			зонснена	BIGAEA
Flight Controller	SN		Secure	Storage?
Remote Controll	er SN		3000-800	0186798
Camera SN			19FOHAKI	A8089C



ID	Oracle	Component	Observable Behavior	Classification	Severity	Remote	Vulnerable Devices
#1	ADB check	dji_sys binary	ADB started (root access)	arbitrary code exec			Mini 2
#2	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#3	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#4	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#5	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#6	crash	flight controller	critical error (drone reboot)	buffer overflow			Mavic Air 2
#7	crash	flight controller	critical error (drone reboot)	denial of service			Mini 2
#8	crash	flight controller	critical error (drone reboot)	denial of service			Mini 2
#9	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#10	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#11	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#12	crash	unknown	critical error (drone reboot)	denial of service			Mini 2
#13	crash	flight controller	critical error (drone reboot)	denial of service			Mavic Air 2
#14	UI change	WiFi chip	change SSID	arbitrary code exec			Mini 2, Mavic 3
#15	UI change	flight controller	change serial number	identity spoofing			Mini 2

Summary: Fuzzing the Drone

• Fuzzing on hardware: Slow & painful but real bugs

• Tailor fuzzer to your target, for example, custom oracles!

Recap: How to analyze drones

Received DroneID packet:

"mkt":16. "wark":0. "sequence_number": 749, "state_info": 8183, "serial_number": 114 "longitude": 7.26717581494; "altitude": 14463117344 "altitude": 14463117344 "wight": 3.66, "w_north": -1, "w_esst": 0, "d_langlet: -14958, "gp_ltes": 14631674322; "applate: 514631674322; "applate: 51463167432; "applate: 5146316743; "applate: 5146316743; "applate: 514631674; "applate



Drone and pilot's location tracking

Wireless Analysis

Recap: How to analyze drones

Received DroneID packet:

"unk": 16, "sequence_number": 749 "state info": 8183. "serial_number": "1W "longitude": 7.26717583494 "latitude": 51.446351119 "altitude": 40.84. "height": 3.66, "v_north": -1, "d_1_angle": -14958, "gps_time": 1649869492647 "app_lat": 51.44631674239 "app_lon": 7.267101350460 "longitude_home": 7.267176 "latitude home": 51.44636 "device_type": "Mavic Air "uuid_len": 19, "uuid": ' "crc-packet": "267c", "crc-calculated": "267c"





Drone and pilot's location tracking

Firmware signature verification bypass

Wireless Analysis

Static Analysis

Recap: How to analyze drones

Received DroneID packet:

"pkt_len": 88, "unk": 16, "version": 2, "sequence_number": 749 "state info": 8183. "serial number": "1W "longitude": 7.2671758349 "latitude": 51.446351119 "altitude": 40.84 "height": 3.66, "v_north": -1, "d_1_angle": -14958, "gps_time": 1649869492647 "app_lat": 51.44631674239 "app lon": 7,26710135046 "longitude_home": 7.26717 "latitude home": 51.44636 "device_type": "Mavic Ai "uuid_len": 19, "uuid": ' "crc-packet": "267c", "crc-calculated": "267c"





ID	Oracle	Component	Observable Behavior	Classification	Severity	Remote	Vulnerable Devices
		dji_sys binary					
		flight controller		buffer overflow			
		flight controller					
		flight controller		buffer overflow			
		flight controller		buffer overflow			
		flight controller					
		flight controller					
		flight controller					
		flight controller					
	UI change		change SSID				Mini 2, Mavic 3
	UI change	flight controller	change serial number				

Drone and pilot's location tracking

Firmware signature verification bypass

Vulnerability detection via fuzzing

Wireless Analysis

Static Analysis

Dynamic Analysis

Takeaways

- Holistic approach (analysis of different components/layers) needed
- Hardware-in-the-loop fuzzing is difficult but rewarding
- Countermeasures seem to be insufficient

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- Holistic approach (analysis of different components/layers) needed
- Hardware-in-the-loop fuzzing is difficult but rewarding
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Moritz: @m_u00d | https://mschloegel.me



Paper

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